(51) International Patent Classification 6 :		(11) International Publication Number: **WO 96/27836**
G06F 15/17	A1	(43) International Publication Date: 12 September 1996 (12.09.96)

(21) International Application Number: PCT/US96/03248

(22) International Filing Date: 7 March 1996 (07.03.96)

(30) Priority Data:
08/399,728   7 March 1995 (07.03.95)   US

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(81) Designated States: AL, AM, AT, AU, AZ, BB, BG, BR, BY, CA, CH, CN, CZ, DE, DK, EE, ES, FI, GB, GE, HU, IS, JP, KE, KG, KP, KR, KZ, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, TJ, TM, TR, TT, UA, UG, US, UZ, VN, ARIPO patent (KE, LS, MW, SD, SZ, UG), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, ML, MR, NE, SN, TD, TG).

Published
With international search report.

(54) Title: SYSTEM AND METHOD FOR EXPANSION OF A COMPUTER

(57) Abstract

A system and method for interfacing at least one ISA and/or PCI compliant device with a PCMCIA compliant device socket (124) on a host computer (122) is disclosed. Preferably included in the system are structures to carry out the functions of: interrupt translation between the PCMCIA port and the ISA devices; direct memory access translation between the PCMCIA port and the ISA devices; translation of bus address latch enable signal between the PCMCIA port and the ISA devices; translation between memory read and memory write signal of the PCMCIA port and a system memory read signal and a system memory write signal of the ISA devices: translation between the PCMCIA port and 8 bit ISA devices; providing a clock signal to the ISA devices; supplying power to the ISA devices; and translation of the timing between the PCMCIA port and the ISA devices.